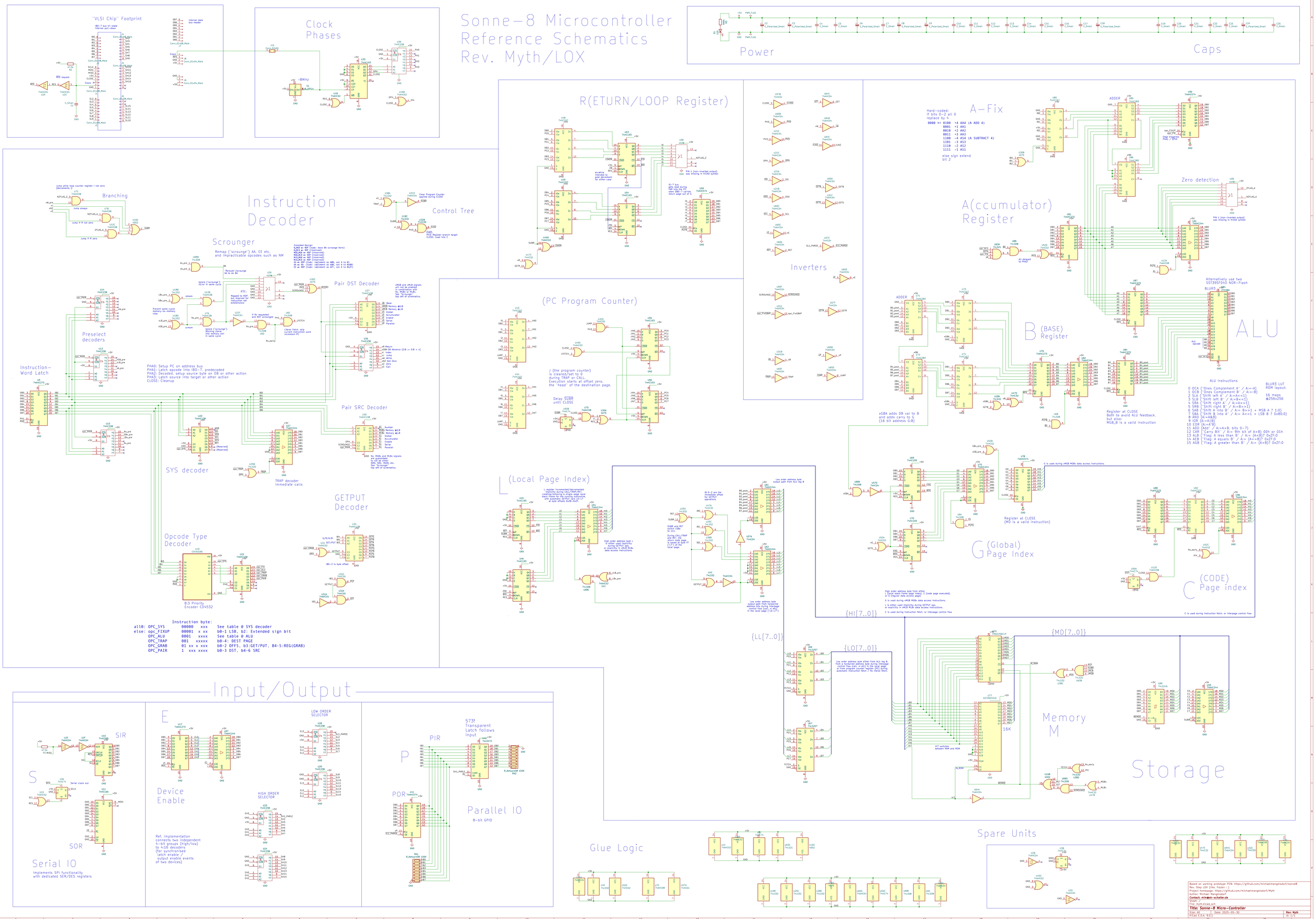
# Sonne-8 Microcontroller Reference Schematics Rev. Myth/LOX




Instruction byte:

| | | | |
|---|---|---|---|
| all0: | OPC_SYS | 00000 xxx | See table @ SYS decoder |
| else: | opc_FIXUP | 00001 x xx | b0-1 LSB, b2: Extended sign bit |
| | OPC_ALU | 0001 xxxx | See table @ ALU |
| | OPC_TRAP | 001 xxxxx | b0-4: DEST PAGE |
| | OPC_GRAB | 01 xx x xxx | b0-2 OFFS, b3:GET/PUT, B4-5:REG(GRAB) |
| | OPC_PAIR | 1 xxx xxxx | b0-3 DST, b4-6 SRC |

Based on working prototype PCB: https://github.com/michaelmangelsdorf/sonne8
Rev. Step LOX (like, frozen :)
Project homepage: https://github.com/michaelmangelsdorf/Myth
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Sheet: /
File: myth.kicad_sch

**Title: Sonne-8 Micro-Controller**

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|---|---|---|
| Size: A3 | Date: 2023-01-31 | Rev: Myth |
| KiCad 6.0.4, 9:1:1 | | Id: 1/1 |