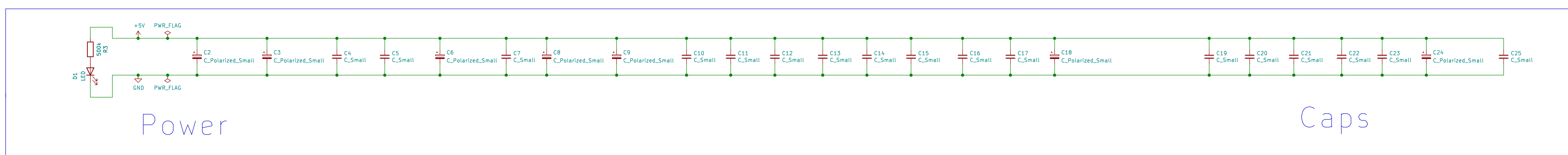
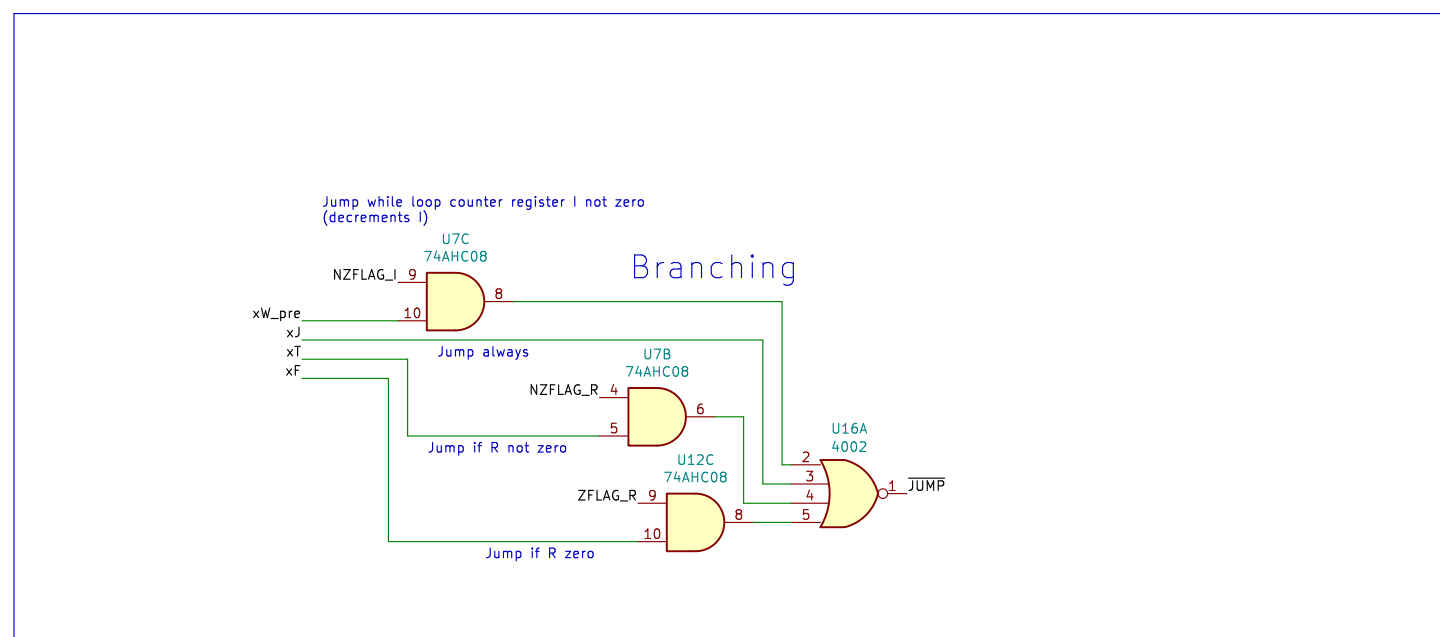


Sonne-8 Microcontroller Reference Schematics Rev. Myth/LOX



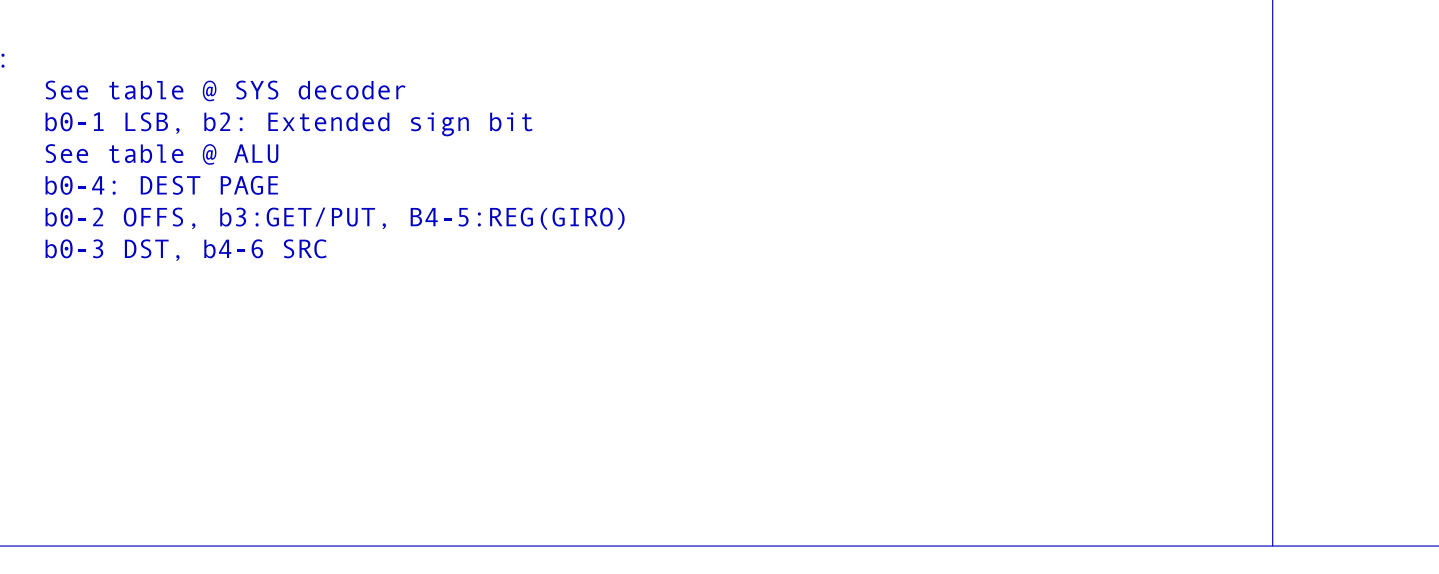
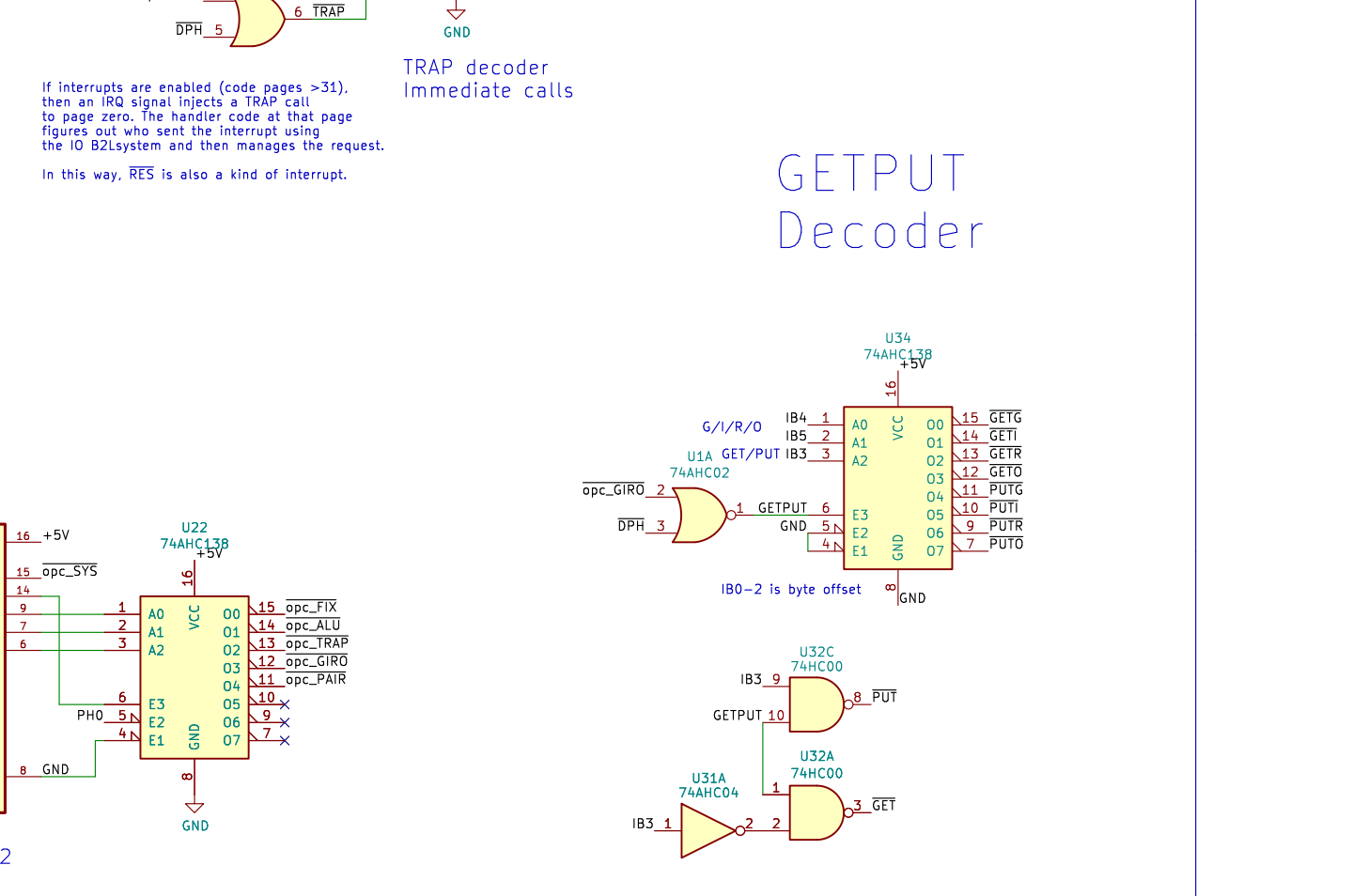
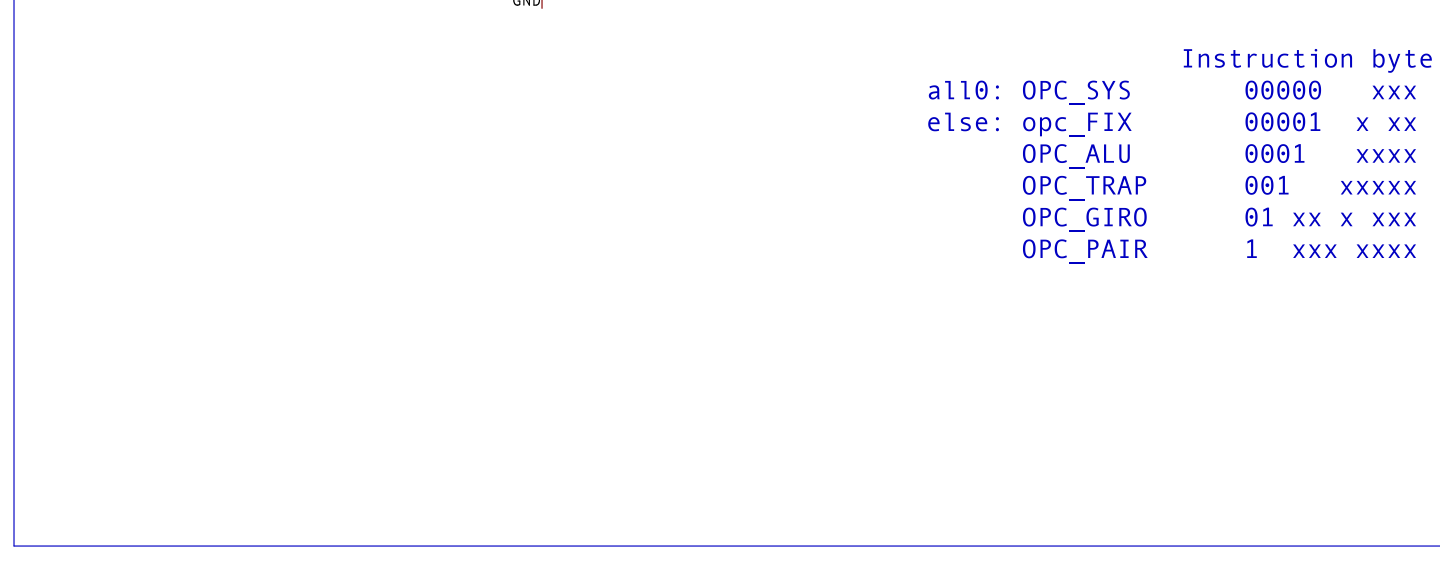
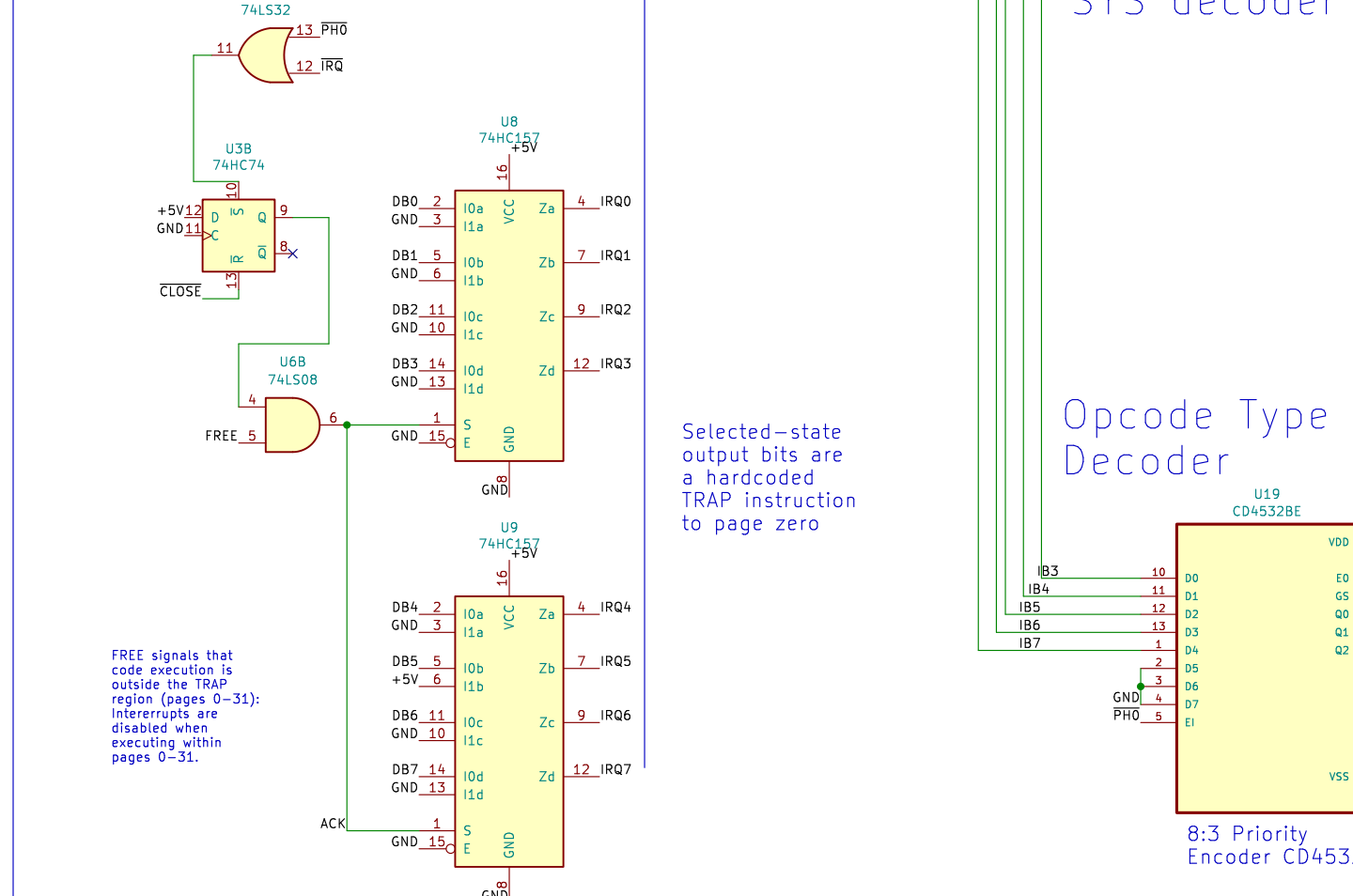
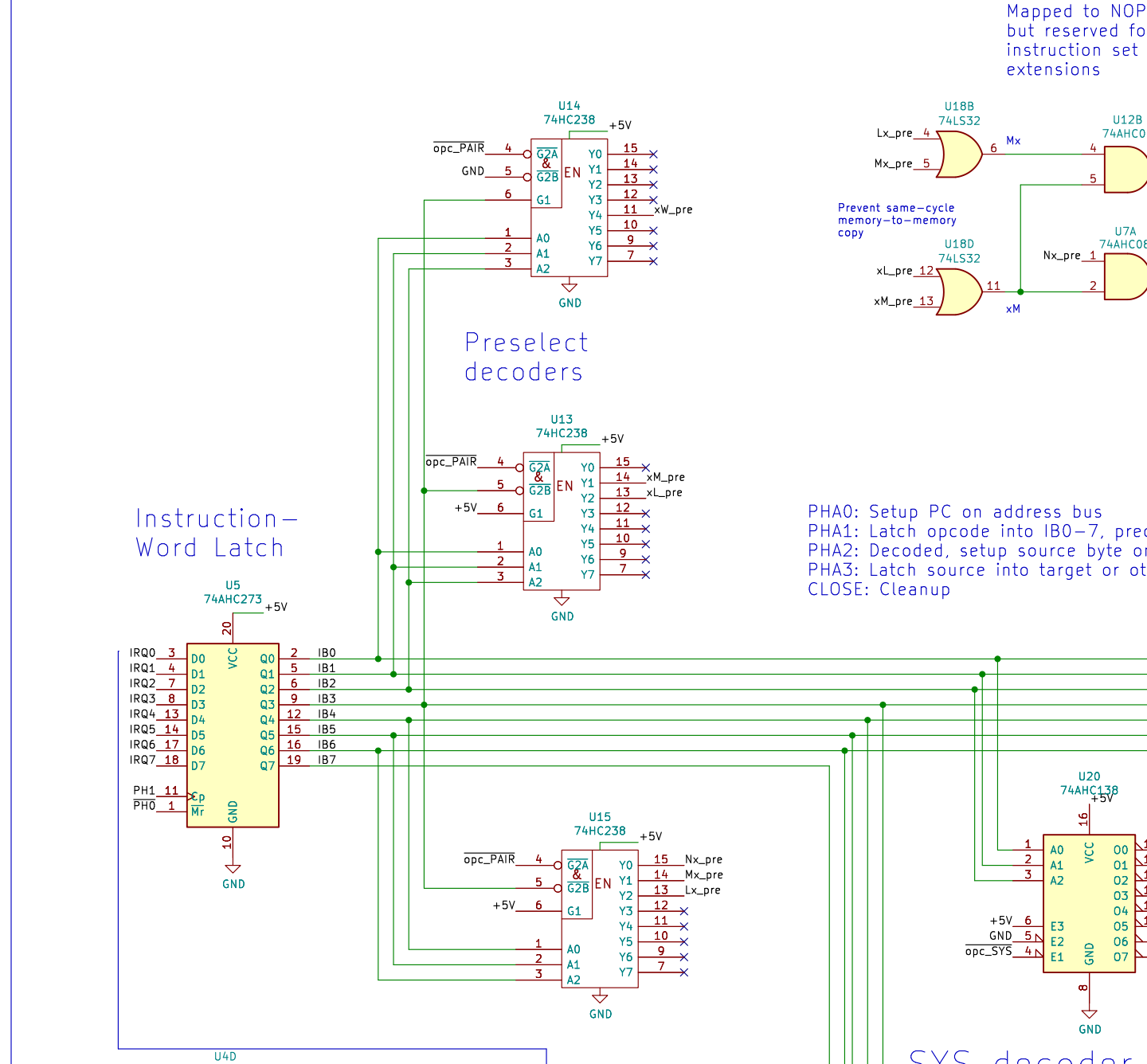
Caps



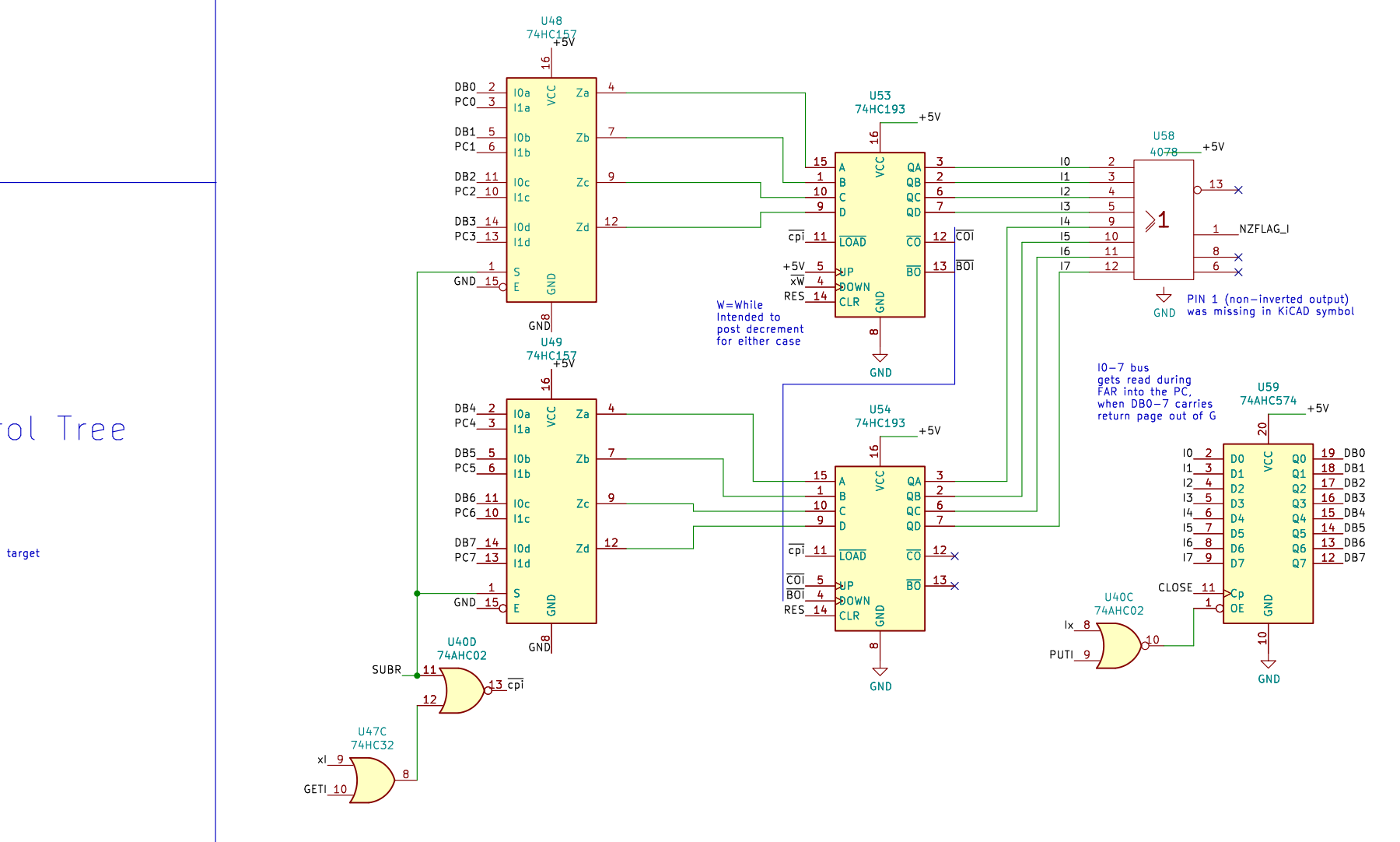
Instruction Decoder

Scrounger

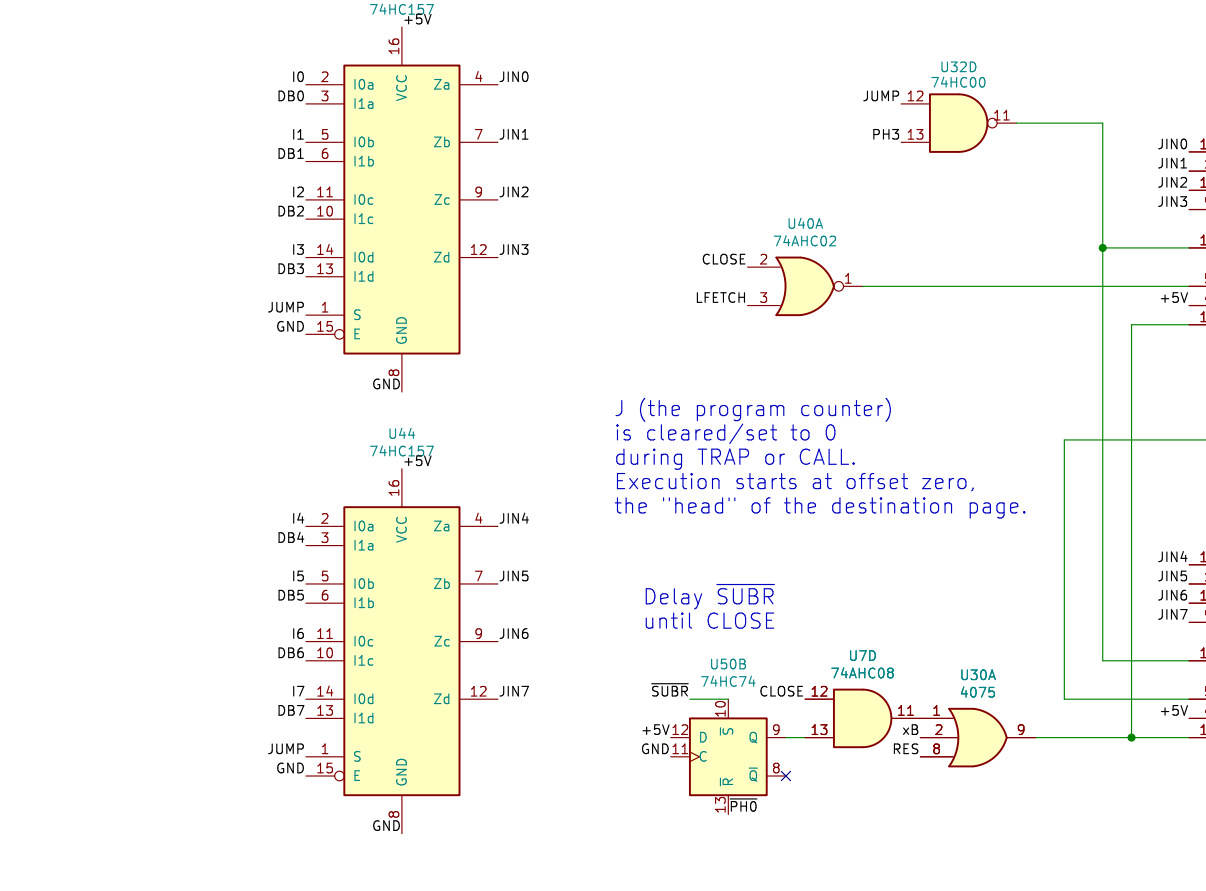
Remap ("scrounger") 00, 08, 11 etc. and impracticable opcodes such as NM



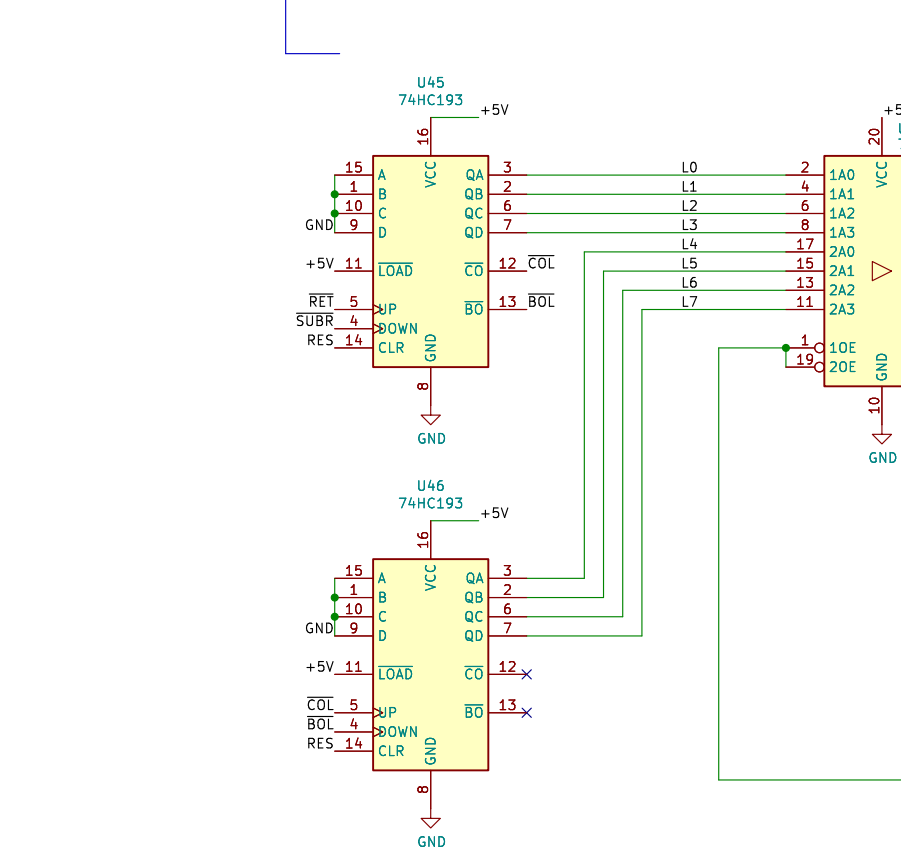
I (INNER Loop Register)



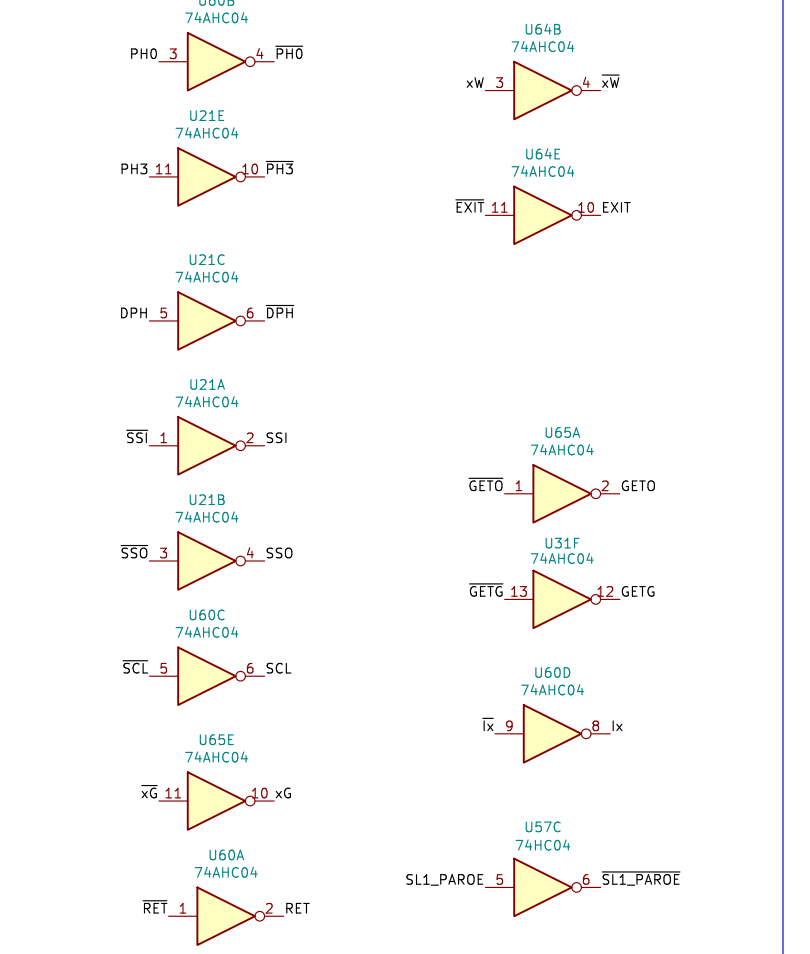
(PC Program Counter)



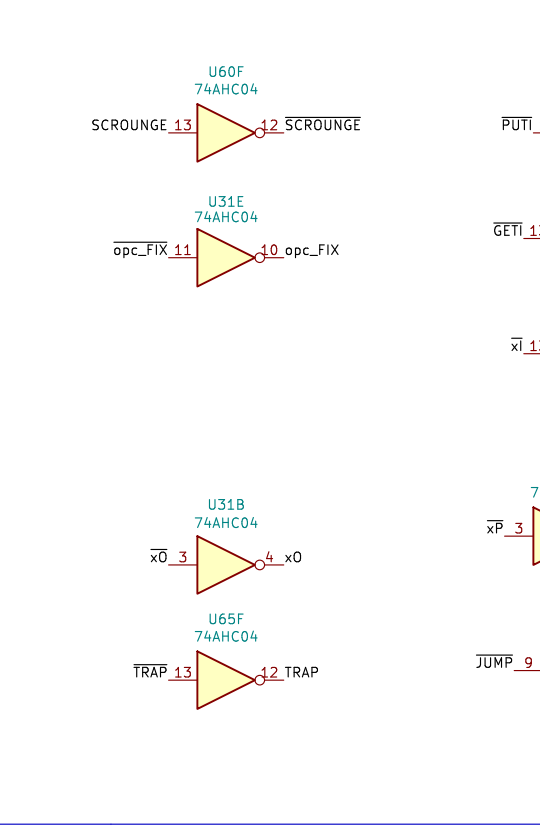
(Local Page Index)



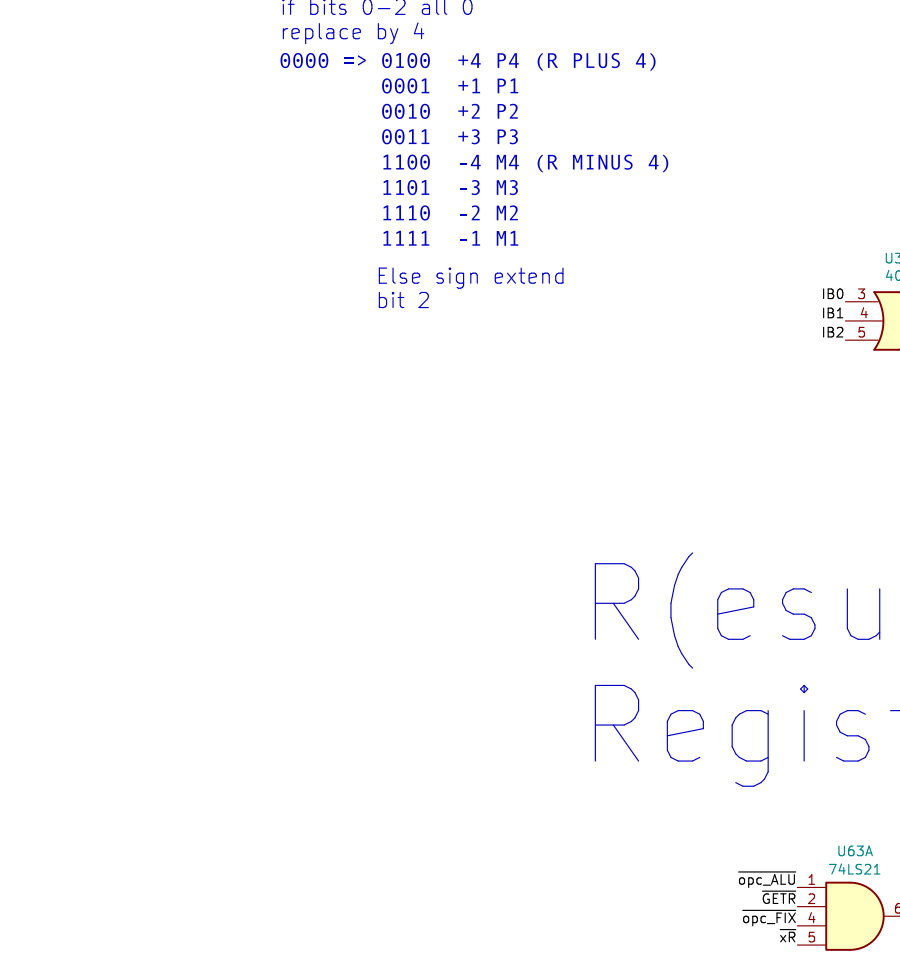
R-Fix



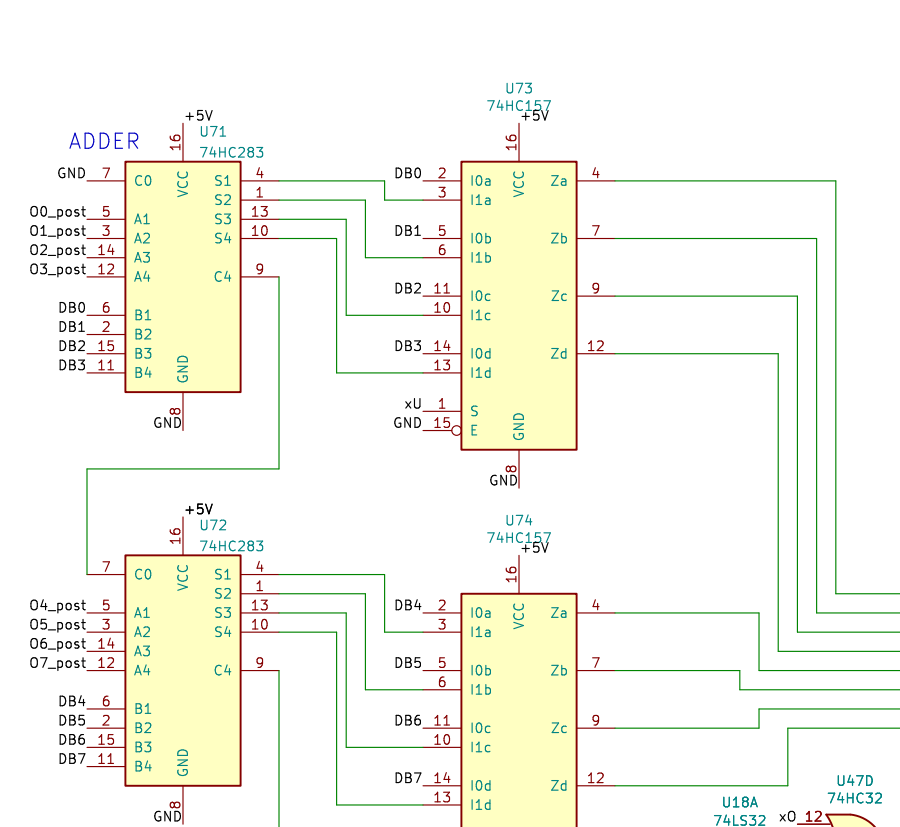
Inverters



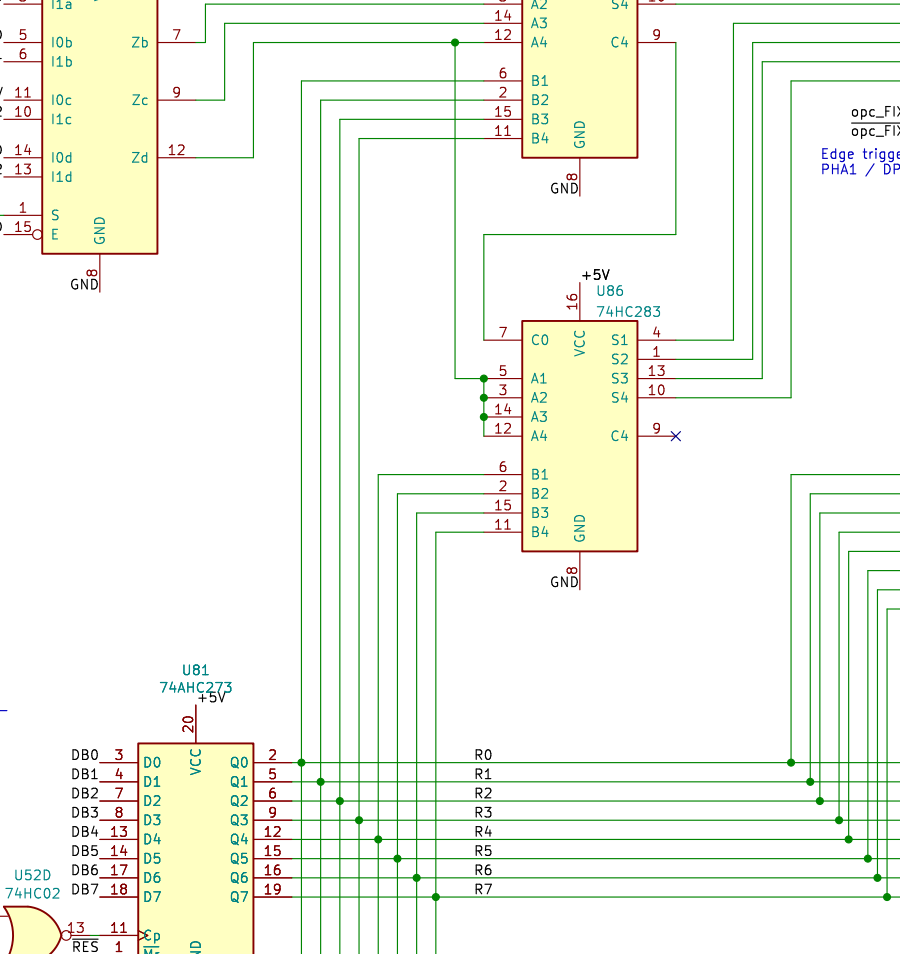
R-Register



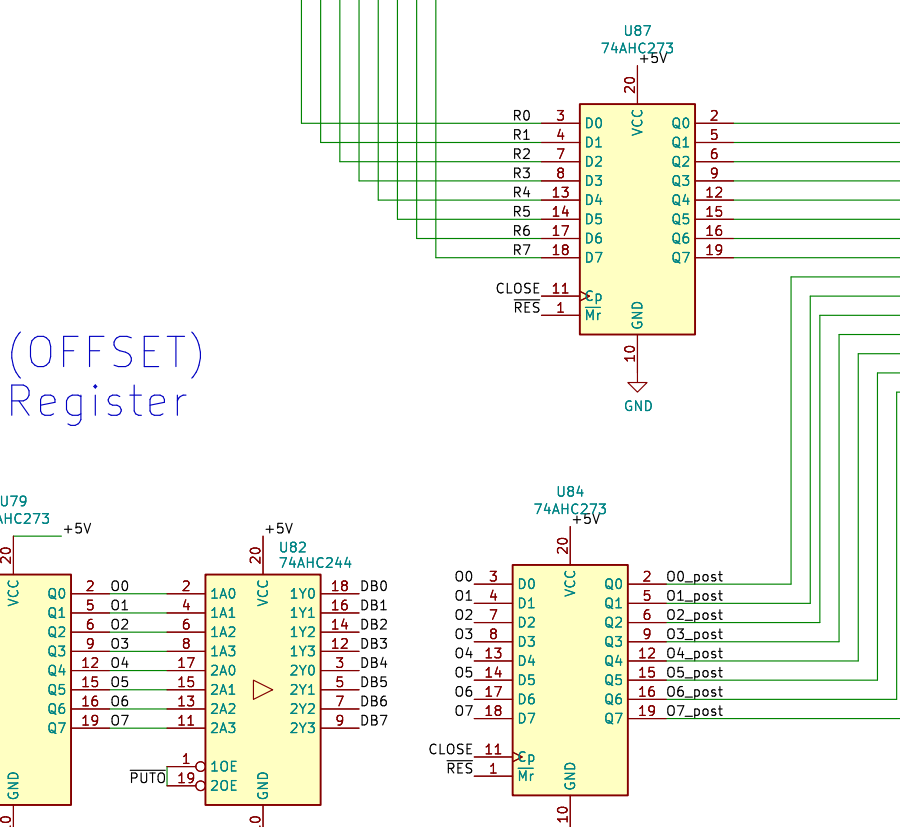
O (OFFSET) Register



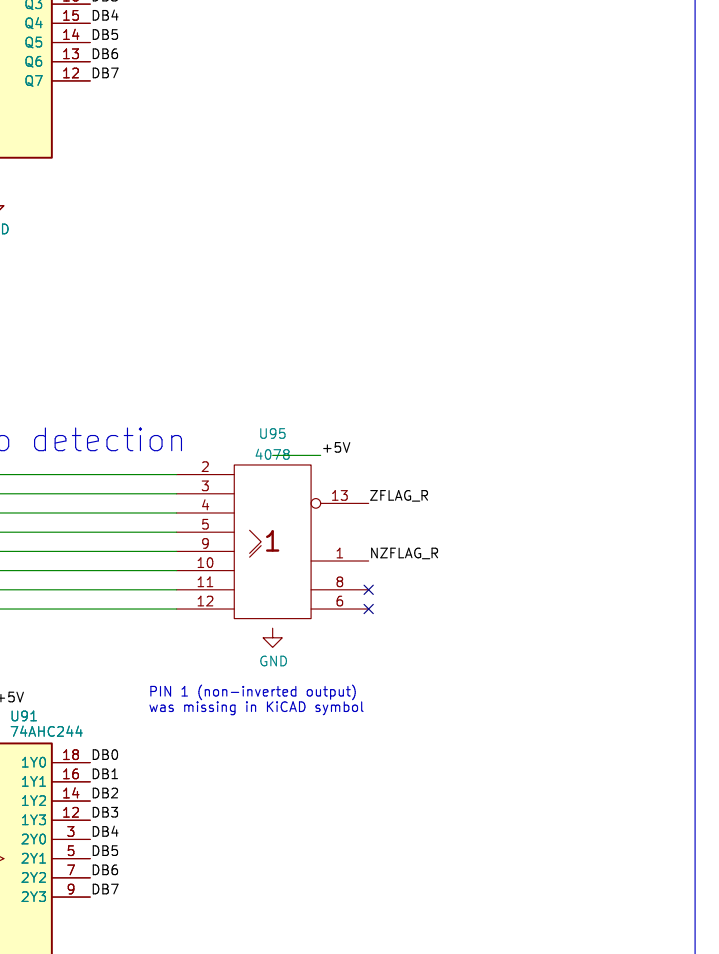
ALU



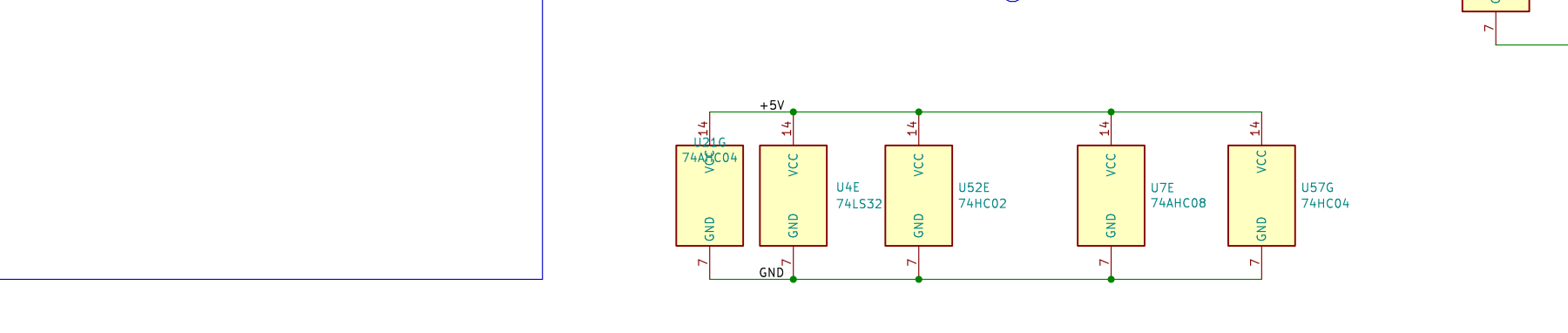
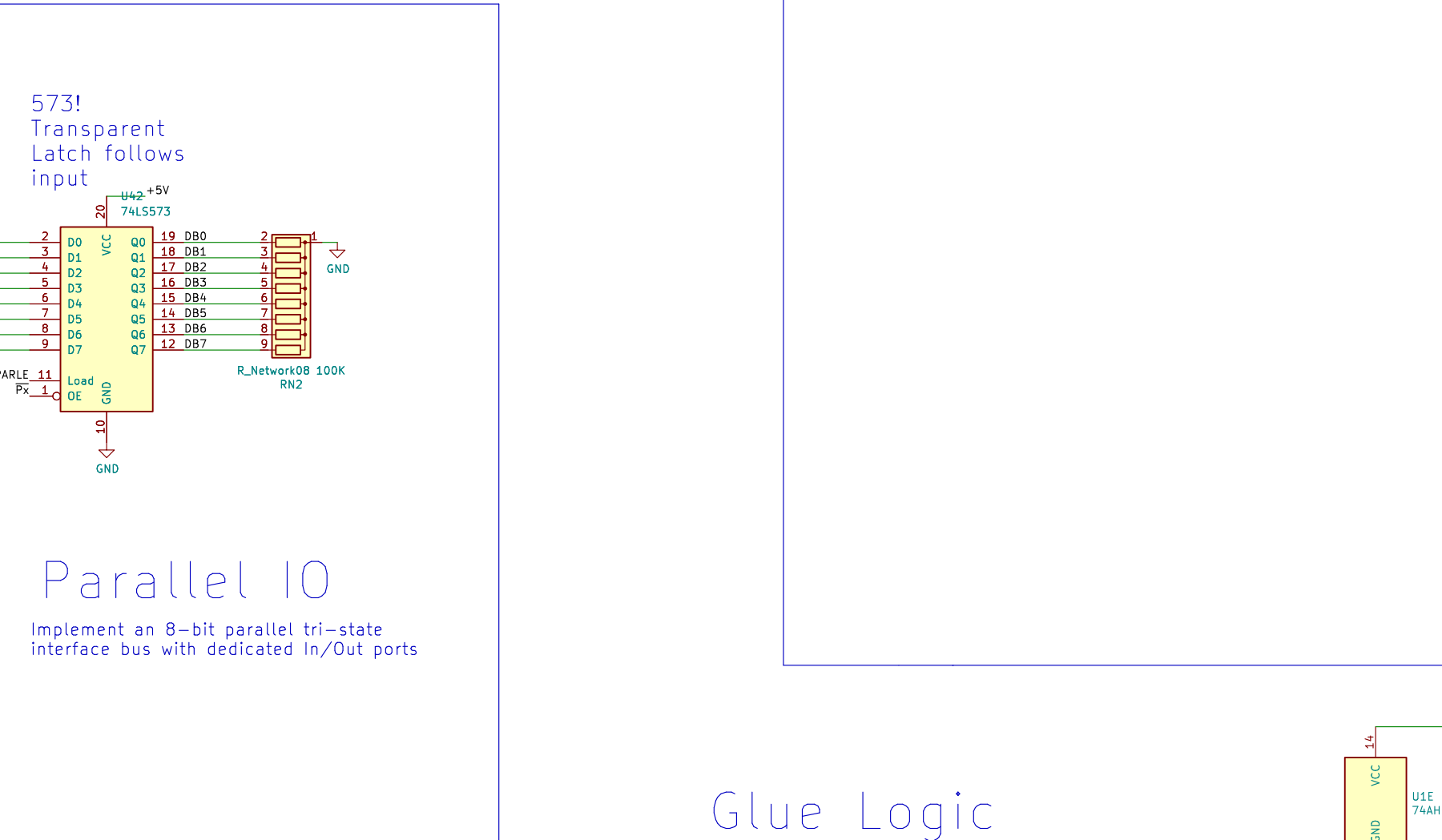
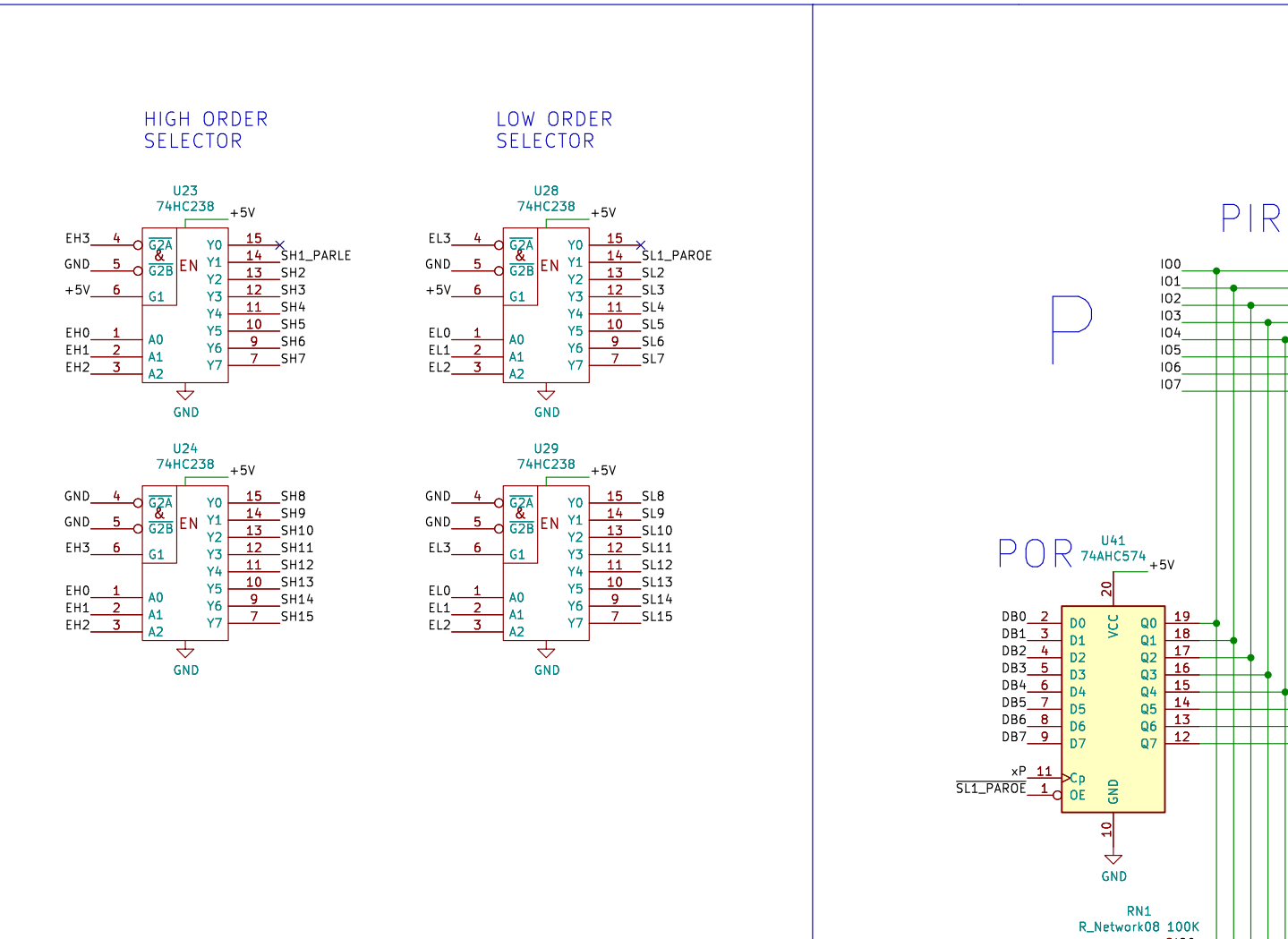
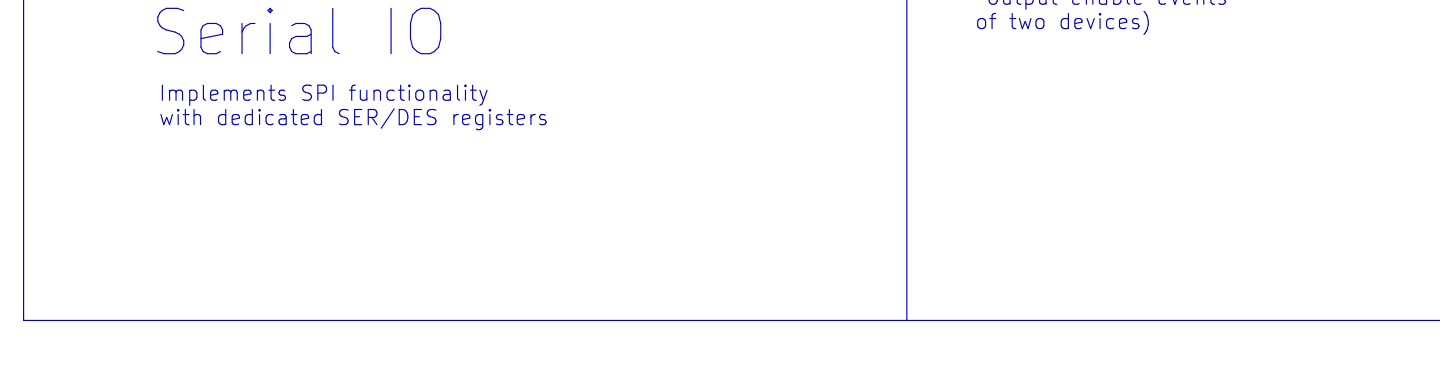
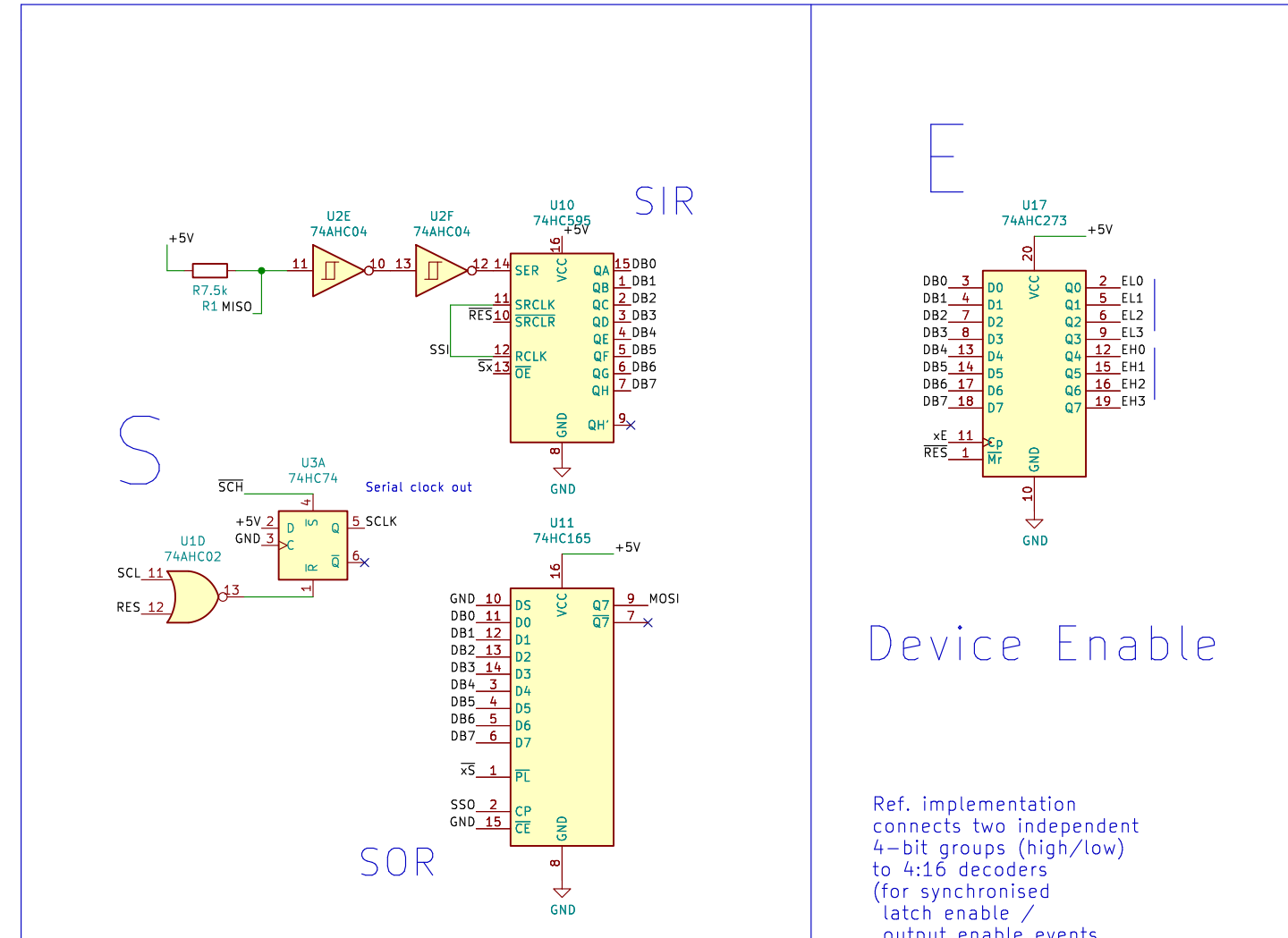
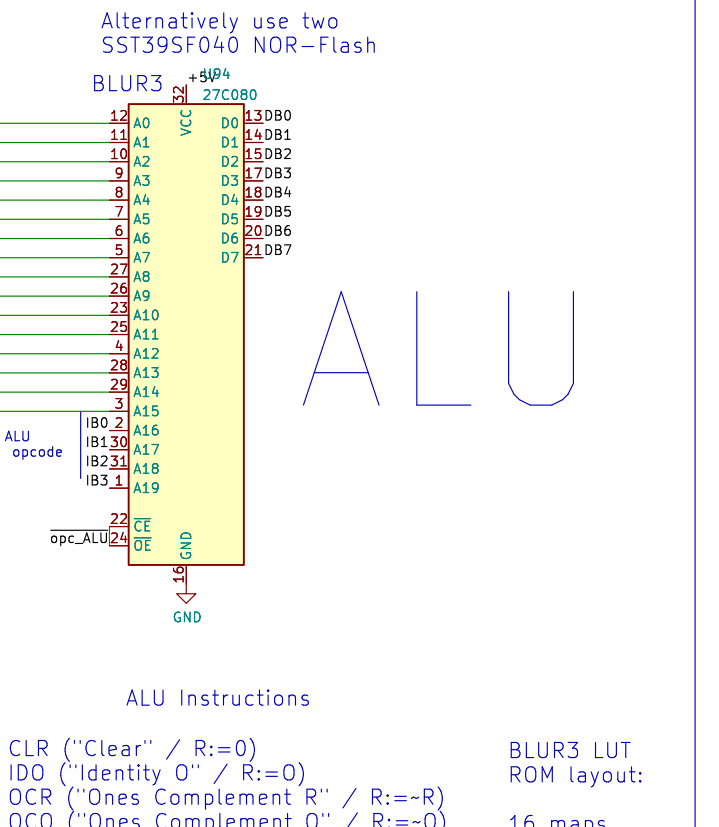
Register at CLOSE



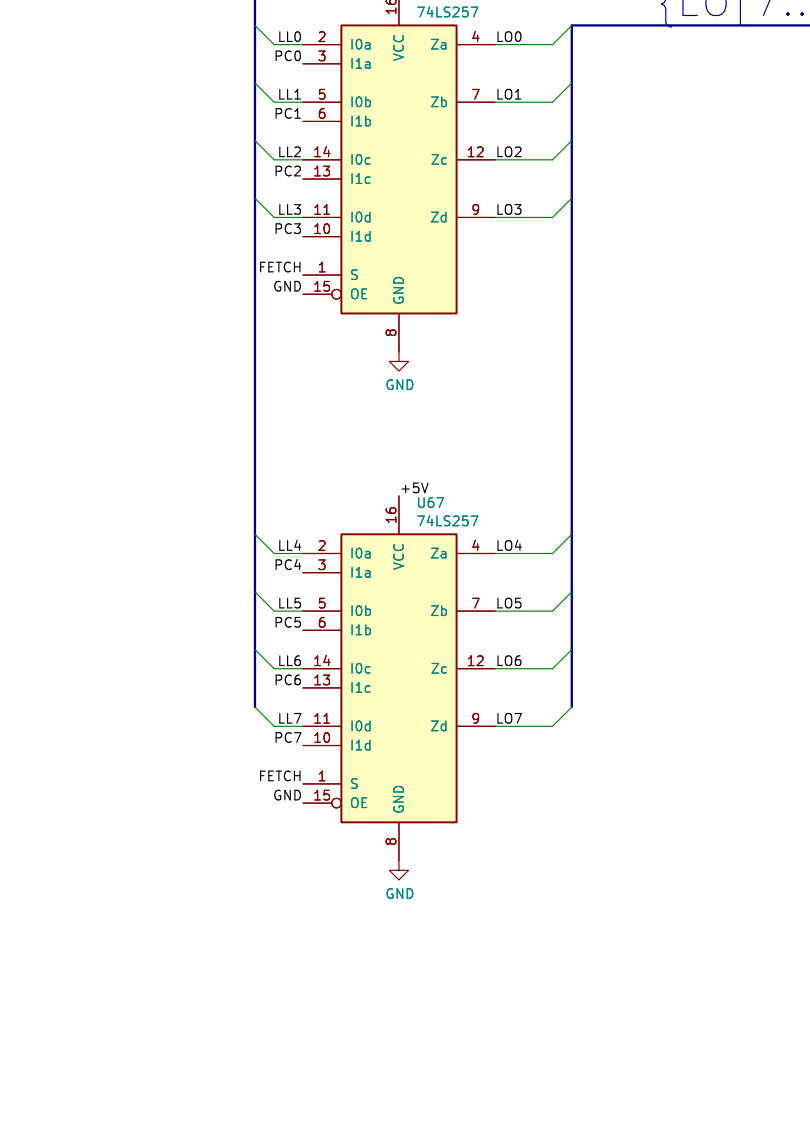
ALU Instructions



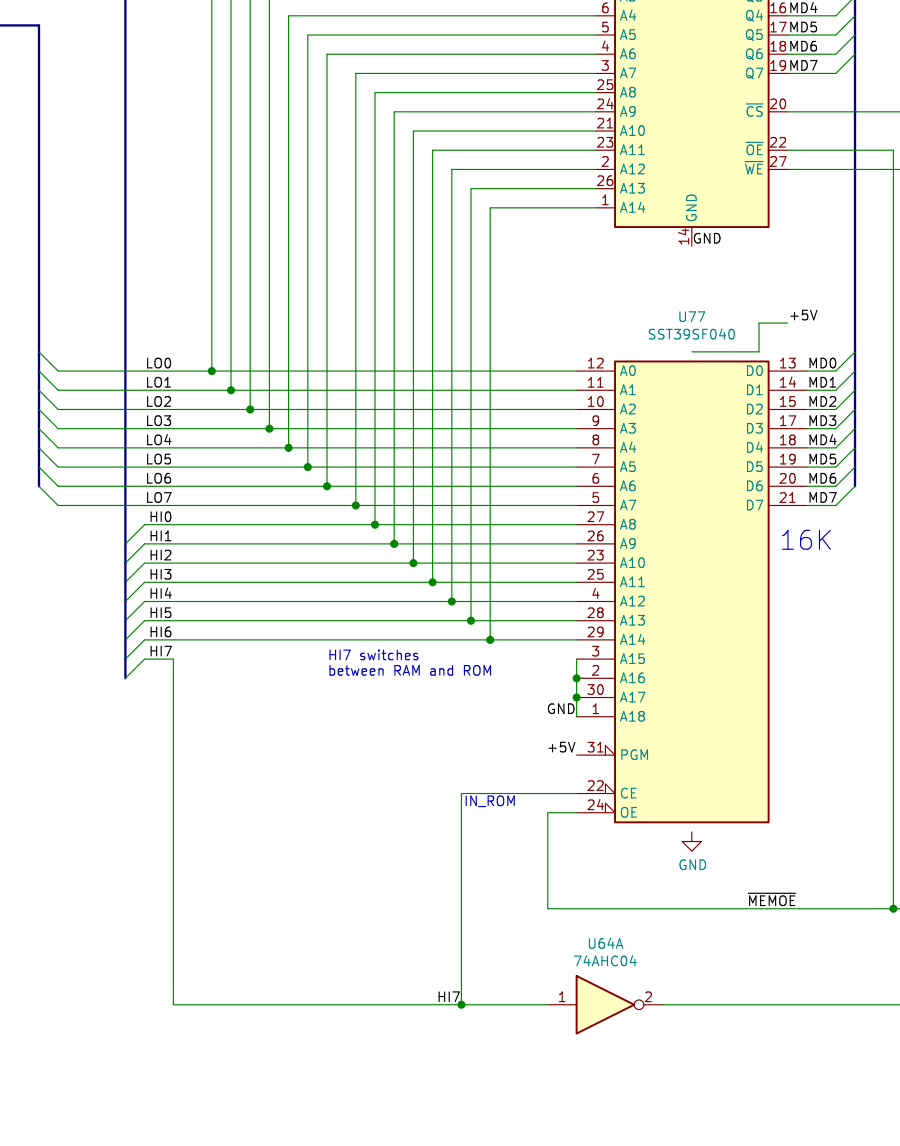
Register at CLOSE



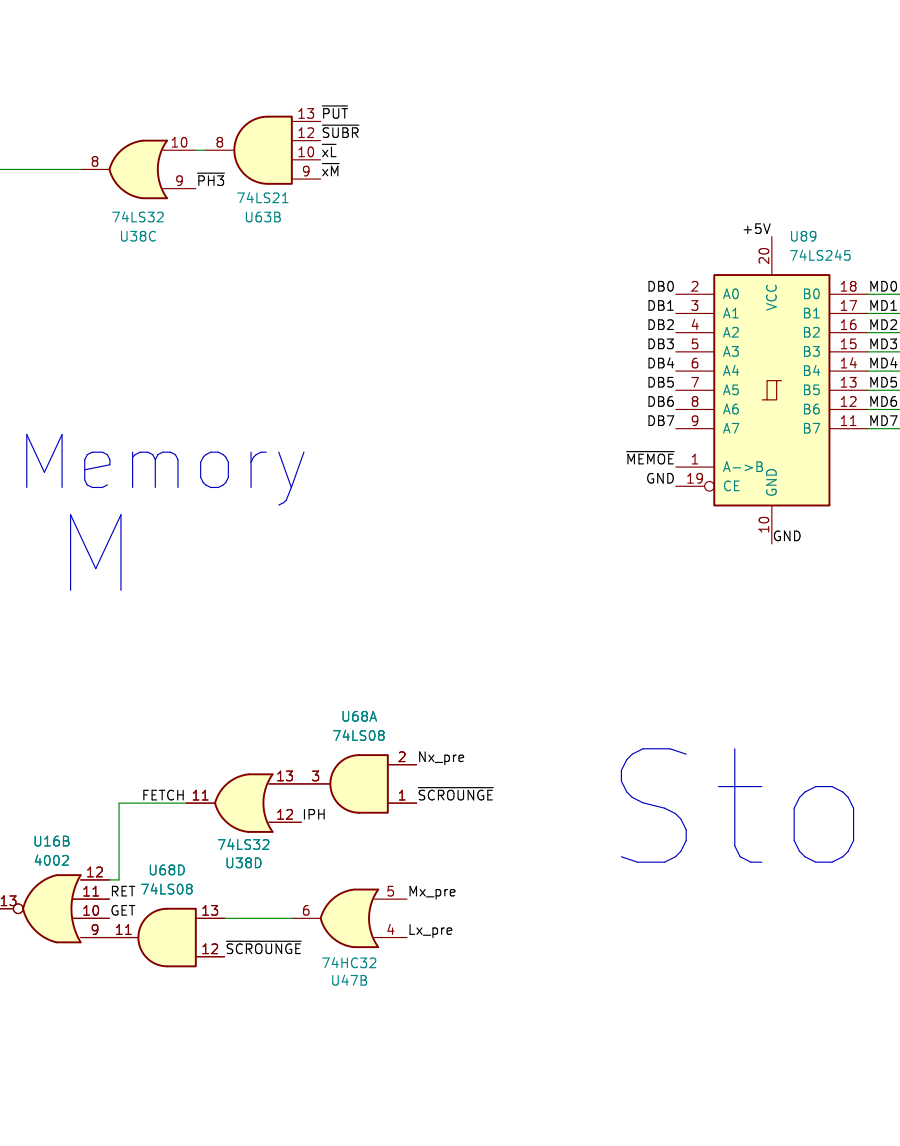
{LL[7..0]}



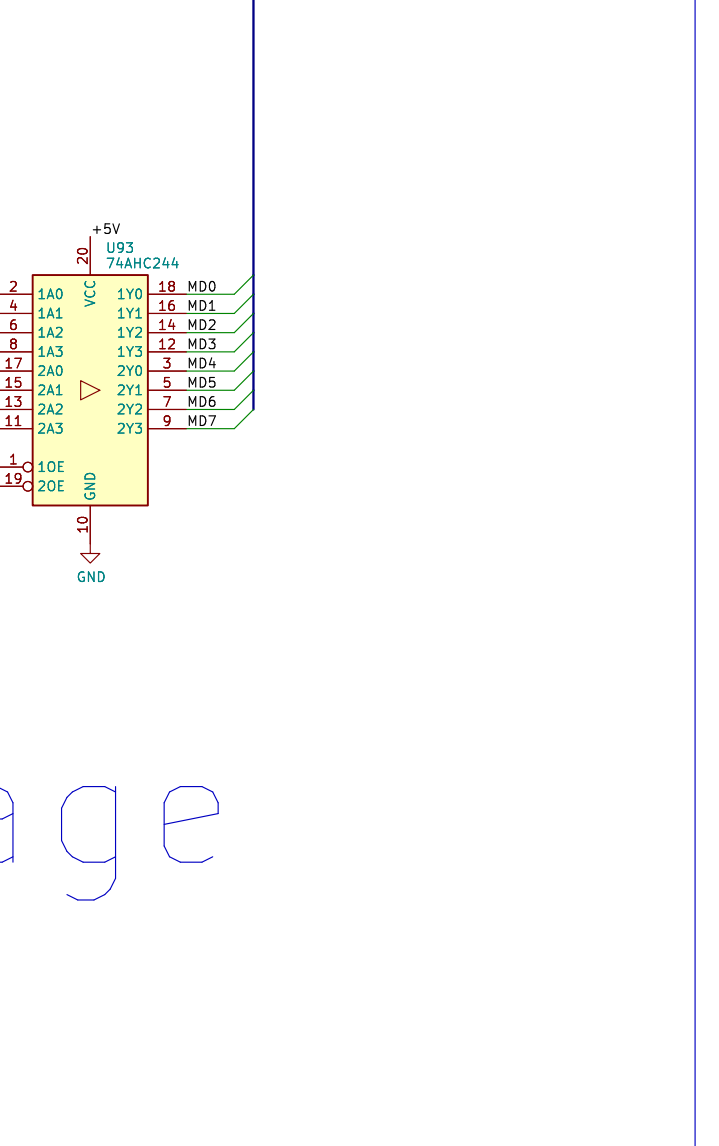
{LO[7..0]}



{MD[7..0]}



Memory M



Storage

Spare Units

